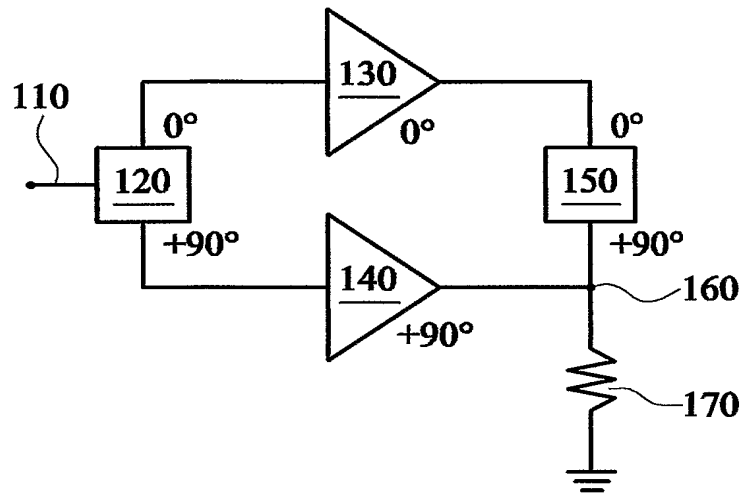
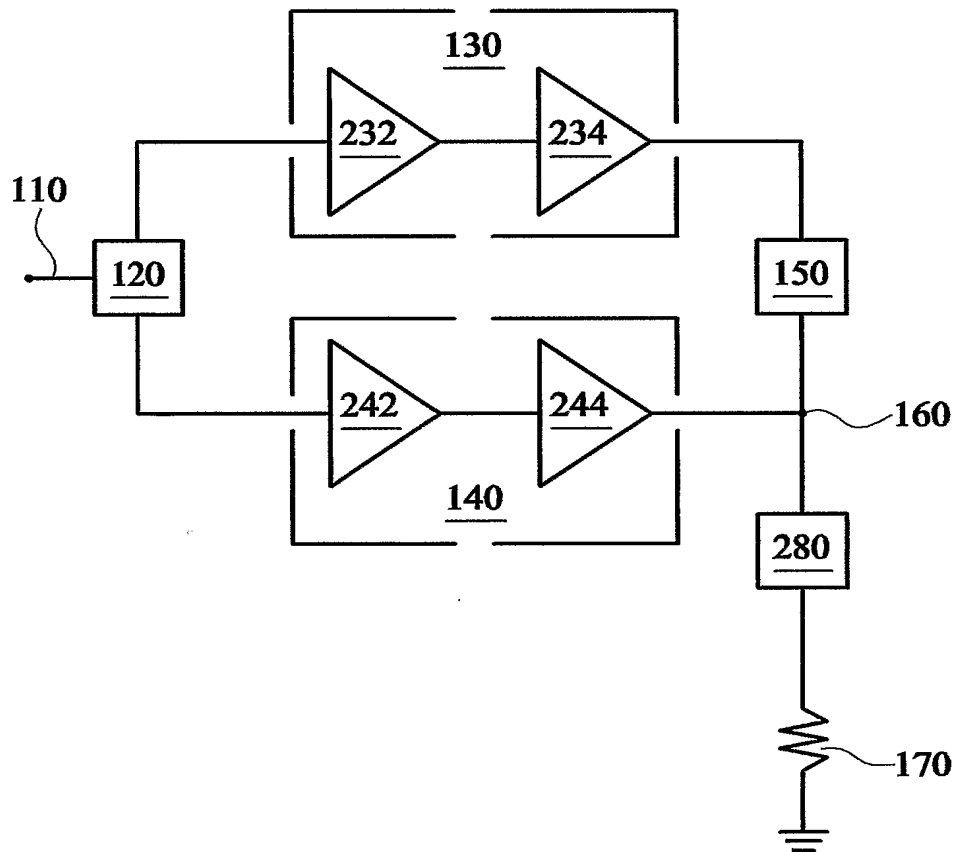


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FIG. 1FIG. 4

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Theoretical plot of output voltage from each device v input drive

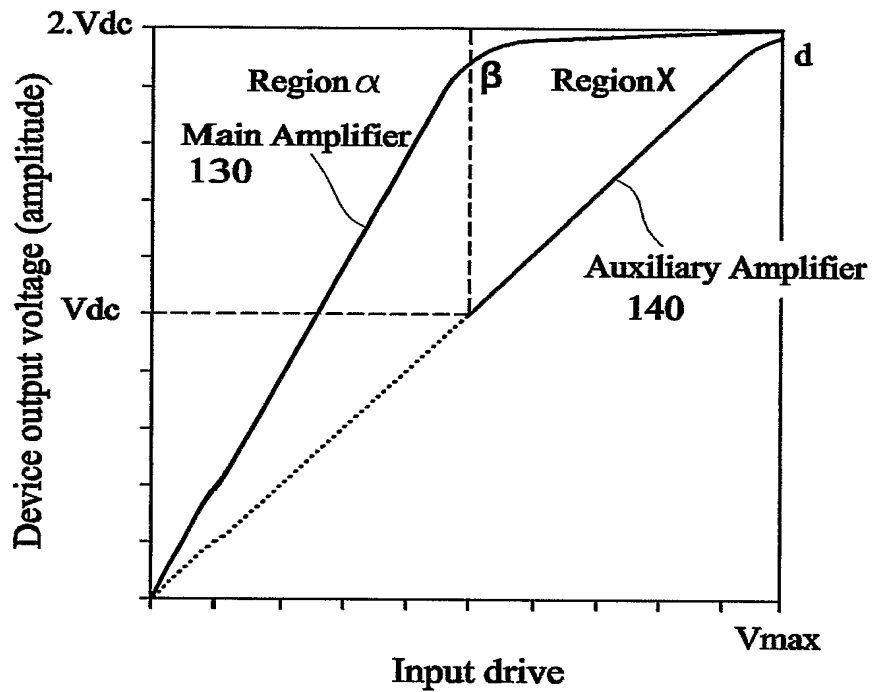


FIG. 2

Theoretical efficiency v output power

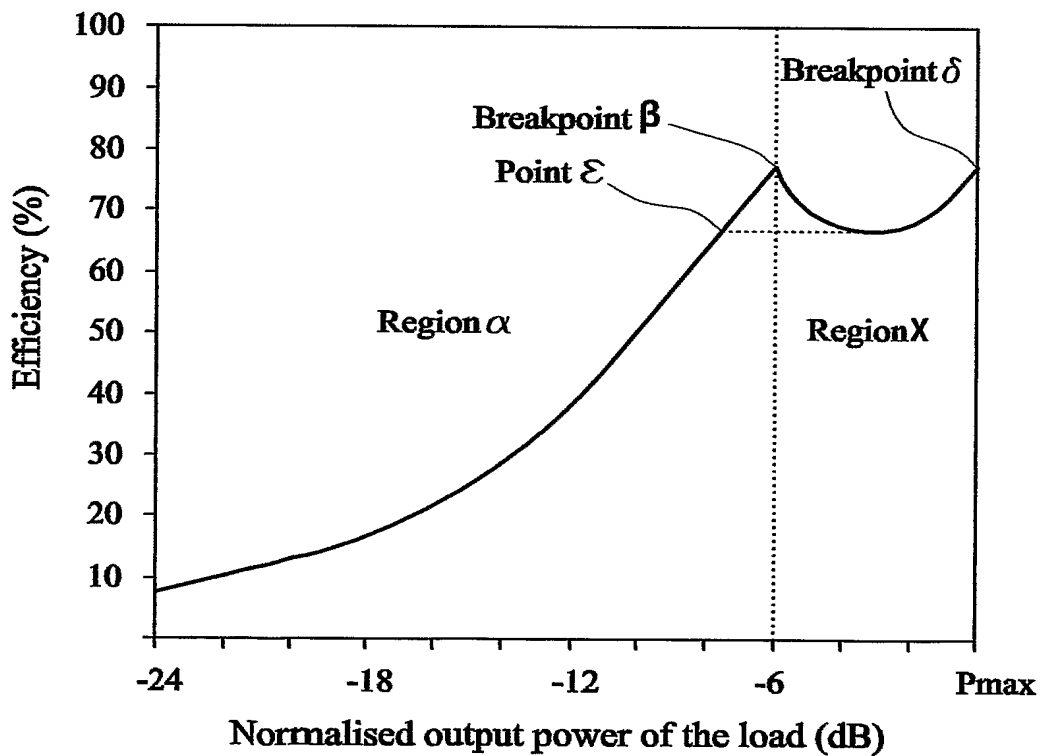


FIG. 3

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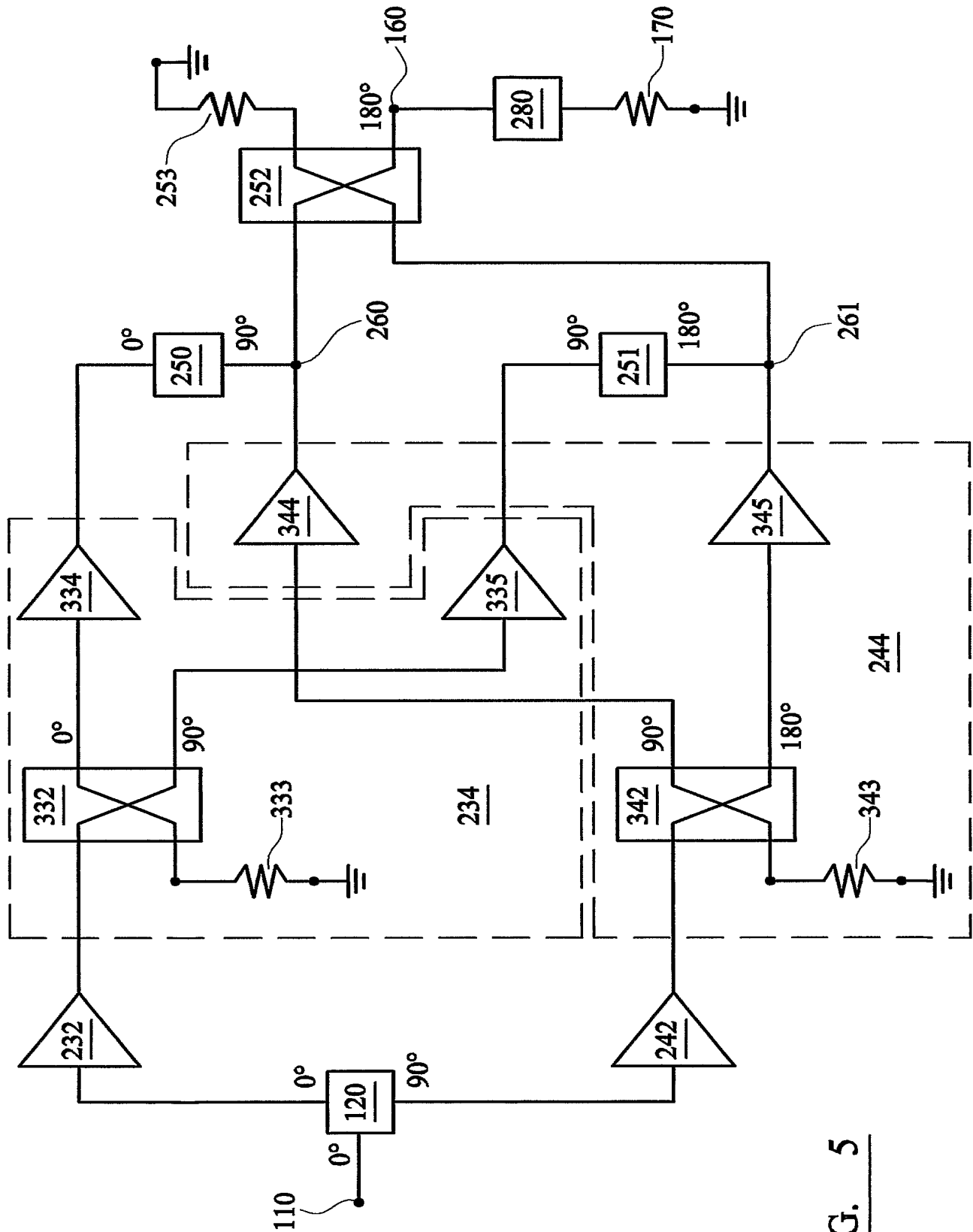


FIG. 5

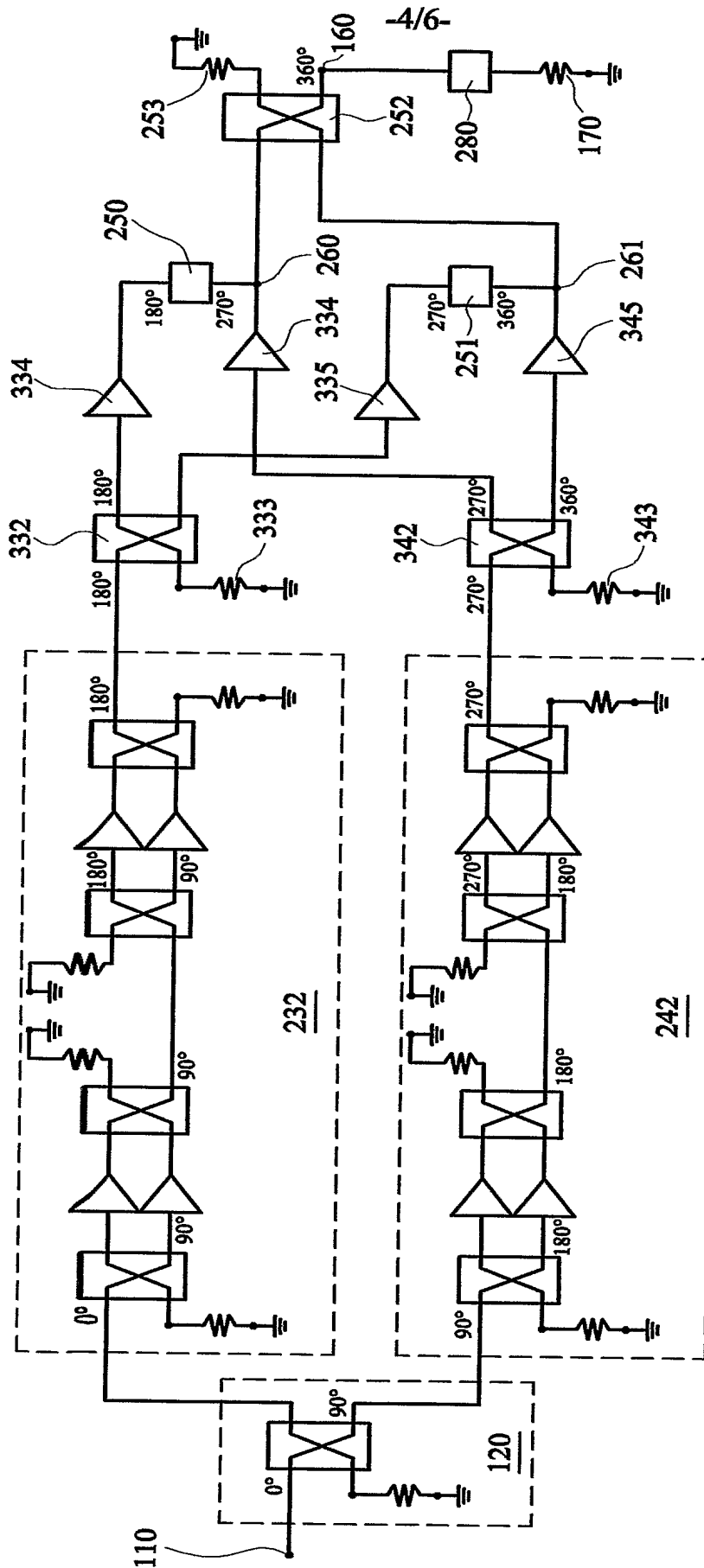


FIG. 6

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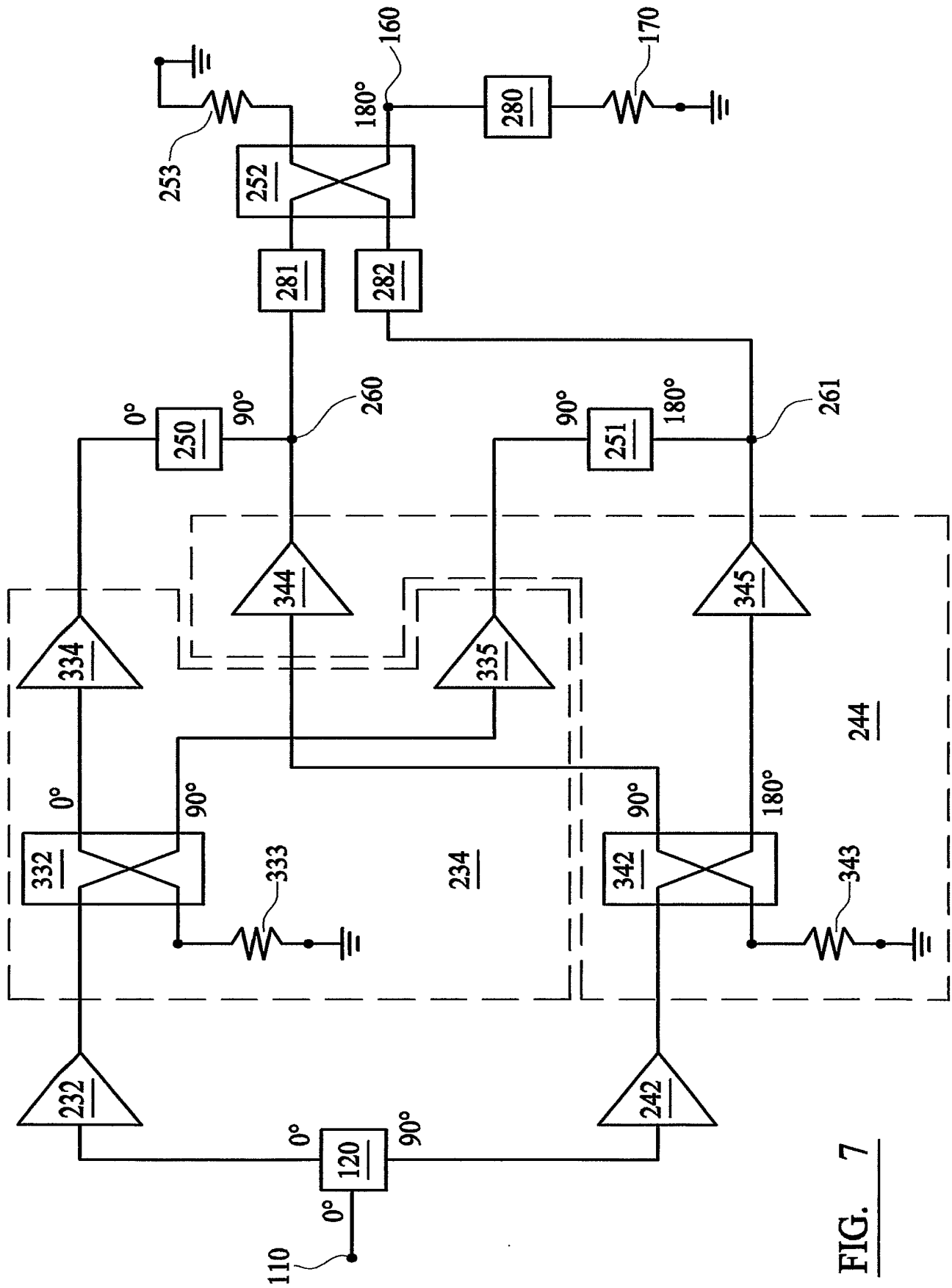


FIG. 7

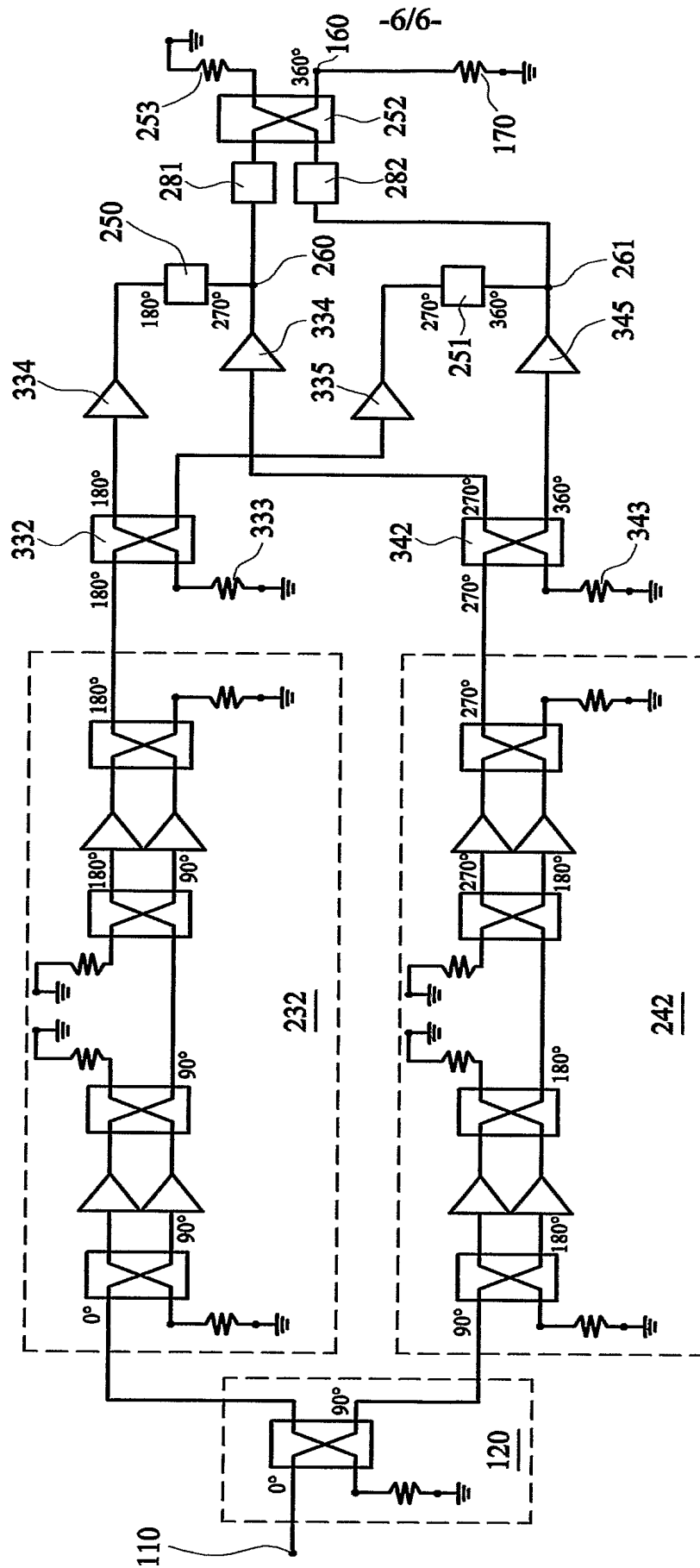


FIG. 8